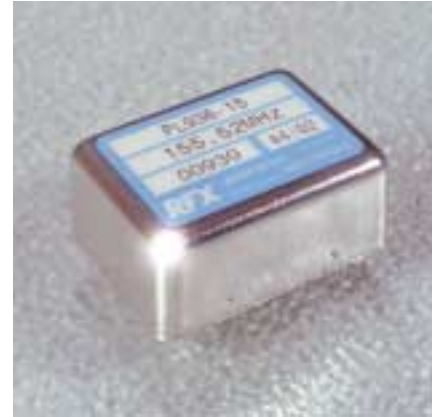


PLL series PL936-15

- 1MHz ~ 2.4GHz
- hybrid PLL TCXO module
- ECL, sine wave output
- phase locked to external precision reference
- sub 1ps jitter

Applications:

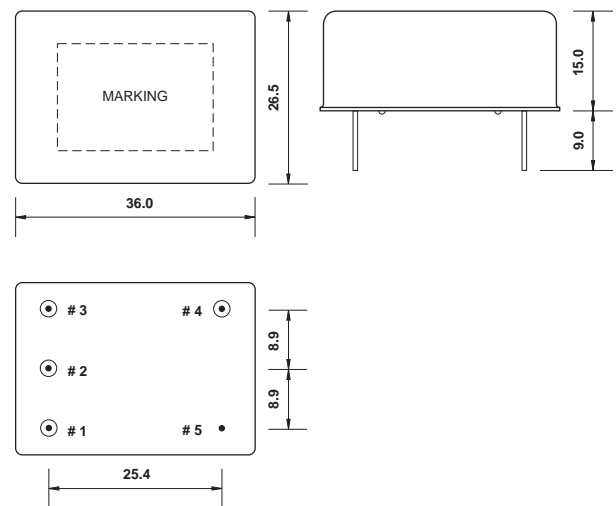
- h.f. communications equipment
- instrumentation
- system synchronisation



Generic specification:

frequency range:	1MHz ~ 2.4GHz
stability:	
TCXO holdover	from ± 1 ppm, custom specified
locked	dependent upon input reference accuracy
input reference	10.000MHz, 0dBm, as standard
against V_{cc} change	± 0.05 ppm max., $V_{cc} \pm 5\%$
against load change	± 0.02 ppm max., load $\pm 10\%$
ageing short term	± 0.005 ppm max./day
ageing long term	from ± 5 ppm max. 10 years
output:	single ended PECL, sine wave, CMOS
lock time	30secs max., frequency dependent
supply voltage:	+3.3Vd.c., +5Vd.c., +12Vd.c., $\pm 5\%$
supply current:	50mA typical
insulation resistance:	500M Ω min., 100Vd.c.
typical phase noise:	
single sideband, 1Hz bandwidth	-100dBc/Hz, $f_o + 10$ Hz -115dBc/Hz, $f_o + 100$ Hz -125dBc/Hz, $f_o + 1$ kHz
jitter:	1ps max., typical
temperature:	
operating range	customer specified
storage range	(-40 +125) $^{\circ}$ C
marking:	part number, frequency, date code, serial number

Dimensions(mm):



Pin connections:

- #1 RF input reference frequency
- #2 lock detect output
- #3 supply voltage, V_{cc}
- #4 RF output
- #5 ground/case

Test circuit, ECL load:

