

100.0000 MHz



Specification:

specification number OS265-1006-S04
frequency: 100.0000 MHz
tolerance: ±0.02ppm(-20 +70)°C
output: LVCMOS
 Duty Cycle 50%±5%

supply voltage: +5.0 Vd.c.

Generic specification:

stability:
 against V_{cc} change ±0.002ppm max., V_{cc} ±5%
 against load change ±0.002ppm max., load ±10%
 ageing short term ±0.002ppm max./day after 30 days continuous operation
 ageing long term ±0.15ppm max./year after 30 days continuous operation
 trim ±1.0 ppm Typical (0 to 3.3V)

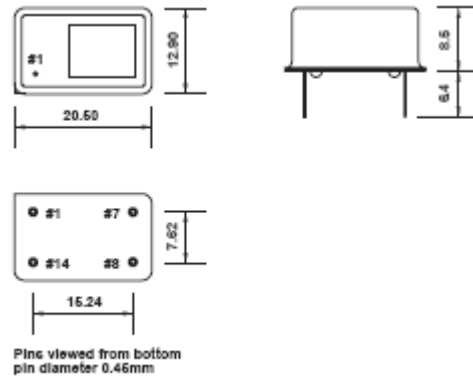
power supplies:
 start up current 420mA max.,
 quiescent current <180mA max.,
 insulation resistance 500MegΩ min., 100Vd.c.

phase noise:
 single sideband -90dBc/Hz, f_o+10Hz
 -120dBc/Hz, f_o+100Hz
 -150dBc/Hz, f_o+1KHz
 -163dBc/Hz, f_o+10KHz
 -165dBc/Hz, f_o+100KHz

temperature:
 operating range (-20 +70)°C
 storage range (-40 +125)°C

marking: part number, frequency, date code, serial number

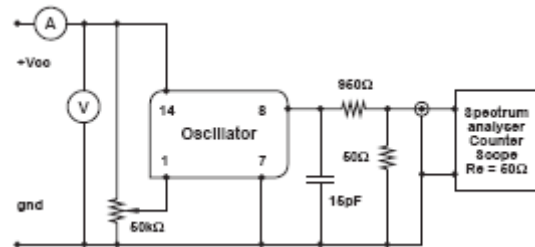
Dimensions (mm):



Pin Connections:

- #1 Trim
- #7 ground/case
- #8 output
- #14 +Vcc

Test circuit:



test circuit includes a 20:1 step down pad.

Environmental conditions:

mechanical shock: MIL standard 202F, method 213, condition J
thermal shock: MIL standard 202F, method 107, condition A
vibration: MIL standard 202F, method 204, condition B
solderability: 5 seconds max. +230°C
 3 seconds max. +350°C
RoHS: RoHS Compliant.



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 oscillators

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